Eleanor M. Kwong

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Objective: To obtain a fun and challenging full time job as a device or process engineer

Education:

University of Minnesota Twin Cities, Institute of Technology (Fall 2002-Spring 2004)
- Masters of Science in Electrical Engineering
- Cumulative GPA of 3.433/4.000 (31 hours of coursework)

University of Illinois at Urbana-Champaign (Fall 1997-Spring 2002)
- Bachelor of Science in Electrical Engineering
- International Minor in Japanese
- Computer Science Minor
- Cumulative GPA of 3.213/4.000 (157 hours of coursework)

Kanazawa Institute of Technology (Summer 2000)
- Participated in an intensive six week Japanese exchange program with a technology focus
- Gave a ten minute oral presentation in Japanese on JIS encoding

Publications:

E. Kai, T. Pan, and B. Ziaie, "A Robust Low-Cost PDMS Peristaltic Micropump with Magnetic Drive", appeared in *Solid-State Senor, Actuator, and Microsystems Workshop*, Hilton Head, USA, 2004.

T. Pan, E. Kai, M. Stay, V. Barocas, and B. Ziaie, "A Magnetically Driven PDMS Peristaltic Micropump", to appear in *26th Annual International Conference IEEE Engineering in Medicine and Biology Society*, San Francisco, USA, 2004.

Projects:

Research (for Masters Plan B Project) under Associate Professor Babak Ziaie
A Robust Low-Cost PDMS Peristaltic Micropump with Magnetic Drive (June 2003-May 2004)
- Designed, created, and tested a silicone elastomer micropump with permanent magnet drive
- Resulted in above publications

Research (for Senior Design Project) under Professor Ilesanmi Adesida
InP and InAlAs Etching for III-V Transistor Applications (November 2001-May 2002)
- Determined the etch rates of InP over InAlAs and vice versa in various acids

Accumulator Project (for Large Scale Integrated Circuit Design course) (Fall 2001)
- Worked with 2 people to design, lay out in IC Station, and simulate using HSPICE an 500MHz accumulator with minimum power dissipation

Experience:

ARRL (American Radio Relay League) Newington, CT
Web Page Maintenance (http://www.arrl.org/) (Summer 1999)
- Redesigned the Technical Information Services and the Public Relations Department pages

UMn Courses:
Semiconductor Properties and Devices I
Semiconductor Properties and Devices II
Physical Principles of Thin Film Technology
Microelectronic Fabrication
Sensors and Transducers (MEMS)

UIUC Courses:
Solid State Electronic Devices
Atomic Physics and Quantum Theory
Large Scale Integrated Circuit Design
Theory and Fabrication of Integrated Circuit Devices
Introduction to VLSI System Design

Skills:

Computer Languages
- Intel x86 assembly, C++, JAVA, HTML, VHDL, Verilog

Software
- Cadence Virtuoso, PSPICE, Synopsys HSPICE, Mentor Graphics IC Station, Matlab